PTO/SB/21 (09-04)

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		Application Number	10/032,332	
TRANSMITTAL		Filing Date	12/20/2001	
FORM		First Named Inventor	Mark Moshayedi	
		Art Unit	2114	
(to be used for all correspondence after initial filing) .		Examiner Name	IQBAL, NADEEM	
otal Number of Pages in This Submission	13	Attorney Docket Number	PREVENT.01A	

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02.11-05

PE pplication No. 10/032,332 pled: December 20, 2001

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Mark Moshayedi)	Group Art Unit 2114
Appl. No.	:	10/032,332 December 20, 2001)	I hereby certify that this correspondence and a marked attachments are being deposited wit the United States Postal Service as first-clas mail in an envelope addressed to: Assistan Commissioner for Patents, Washington, D.C. 20231, on
Title	:	SYSTEM AND METHOD FOR PREVENTING DATA CORRUPTION IN SOLID-STATE MEMORY DEVICES AFTER A POWER FAILURE)	Seyed Jalal Sadr Recognized to practice under Under 37 CFR § 10.9(b)
Examiner	:	Iqbal, Nadeem)	

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION DATED NOVEMBER 9, 2004

Dear Sir:

In response to the Examiner's Office Action mailed November 9, 2004, please amend the above-identified application as follows: